



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jeng-Yang Pan

Application No.: 10/013,089 (CONF 1424)

Filed: 12/7/2001

Title: Semiconductor wafer tray positioning

Attorney Docket No.: 67,200-627

2813  
#7/B  
10/28/03  
Am H  
Group Art Unit:  
2813

Examiner:  
Jennifer M. Dolan

Commissioner for Patents  
Alexandria, VA 22313

Certificate of Mailing

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313.

Date: Oct. 10/2003

*[Signature]*  
Kathy Dixon

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OCT 17 2003  
TECHNOLOGY CENTER 2800

RESPONSE TO NON-COMPLIANT AMENDMENT

Dear Sir:

In response to the Notice of Non-Compliant Amendment dated October 1, 2003, re-submitted herewith is the corrected listing of claims.